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10/066,150  
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PTO/SB/97 (09-04)  
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• Statement of the Substance of Applicant  
Initiated Interview in Accordance with  
C.F.R. § 1.133(b) (pp2)

on February 6, 2006  
Date

  
Signature

Margaret A. Jesz

Typed or printed name of person signing Certificate

N/A

(972) 480-8800

Registration Number, if applicable

Telephone Number

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PATENT

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: Hung T. Nguyen

Serial No.: 10/066,150

Filed: October 26, 2001

Confirmation No.: 3566

Title: EFFICIENT INSTRUCTION PREFETCH MECHANISM FOR  
DIGITAL SIGNAL PROCESSOR AND METHOD  
OF OPERATION THEREOF

Grp./A.U.: 2181

Examiner: Meonske, Tonia L.

Commissioner of Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

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Sir:

STATEMENT OF THE SUBSTANCE OF APPLICANT INITIATED INTERVIEW IN  
ACCORDANCE WITH C.F.R. § 1.133(b)

Summarized below is the substance of a telephone interview between Examiner Tonia Meonske, and the undersigned agent of record, held on February 6, 2006.

In the telephone interview, Examiner Meonske and Mr. Ralston discussed proposed amendments to Claims 1, 8 and 15 to distinguish the Claims over the cited combination of U.S.

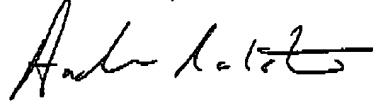
Appl. No. 10/066,150  
Summary of February 6, 2006 Examiner Interview

Patent No. 5,784,603 to Leung, and U.S. Patent No. 4,910,664 to Arizono. Examiner Meonske suggested that a limitation that clarifies that the invention prevents prefetch of *any* instructions beyond the end of an instruction loop in prefetched instructions in cache memory would clarify the distinction between the invention and the cited combination. She further indicated that such language would change the scope of the claims and require a new search.

The Applicants request the Examiner to telephone the undersigned attorney of record at (972) 480-8800 if such would further or expedite the prosecution of the present application.

Respectfully submitted,

HITT GAINES, P.C.



Andrew Ralston  
Registration No. 55,560

Dated: February 6, 2006